

FIG. 1

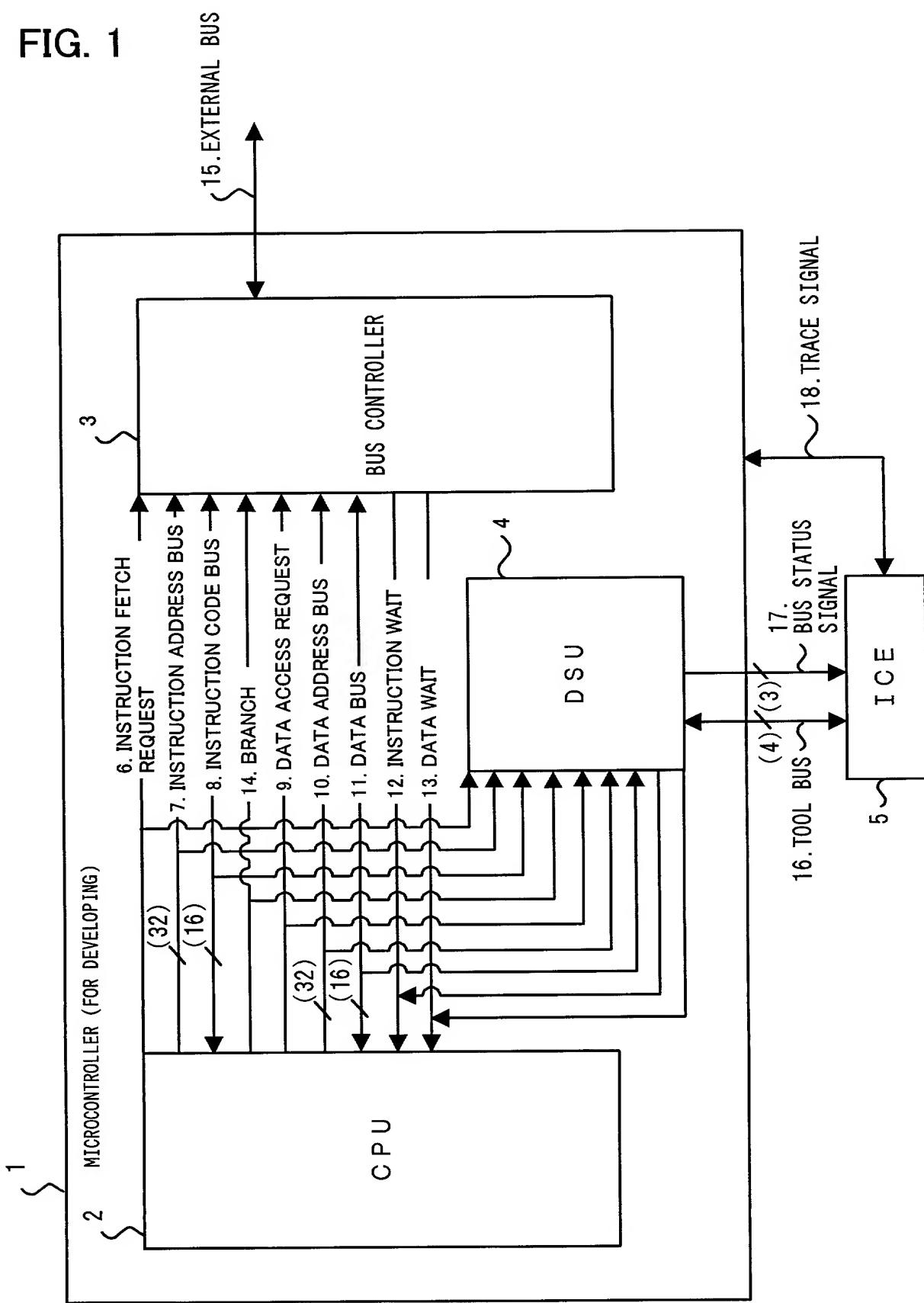


FIG. 2A

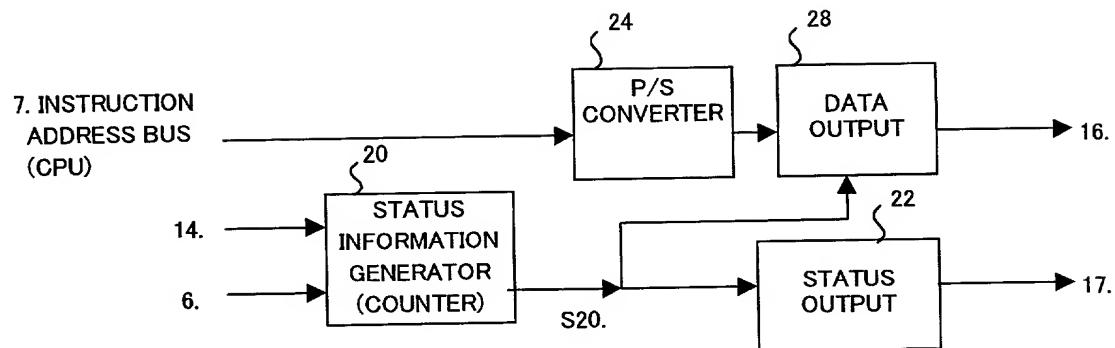


FIG. 2B

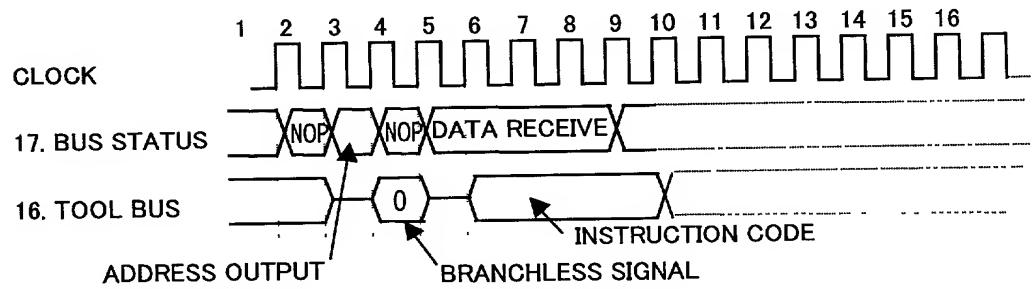


FIG. 2C

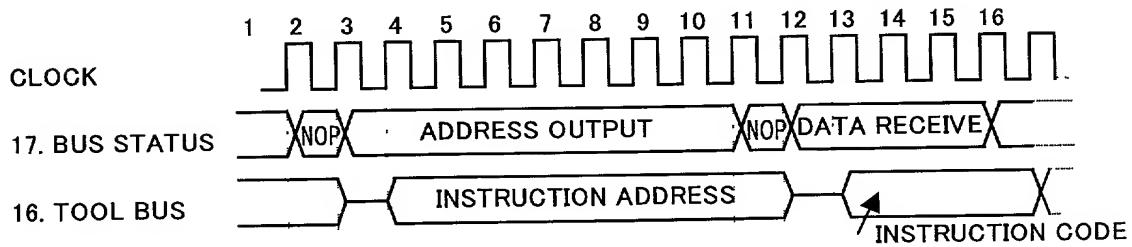


FIG. 3A

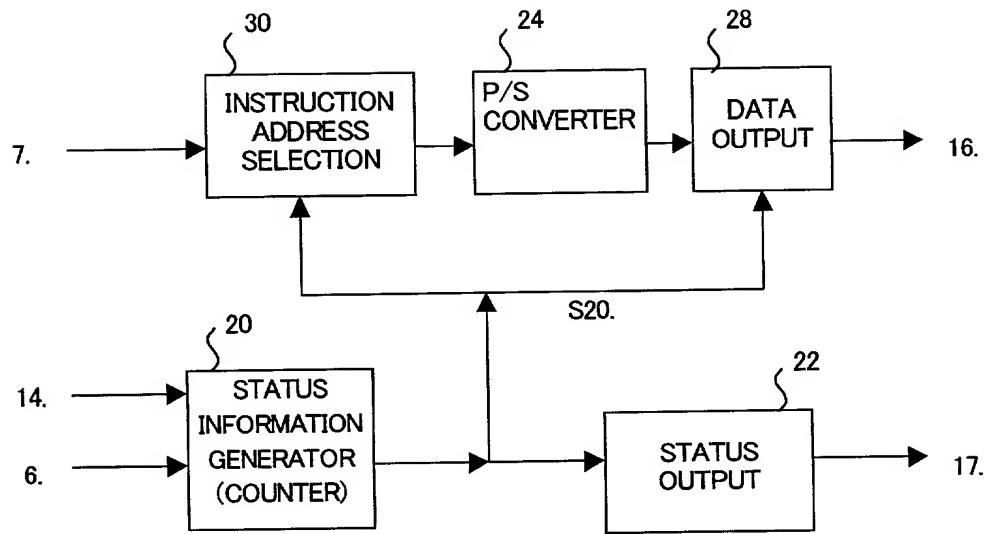


FIG. 3B

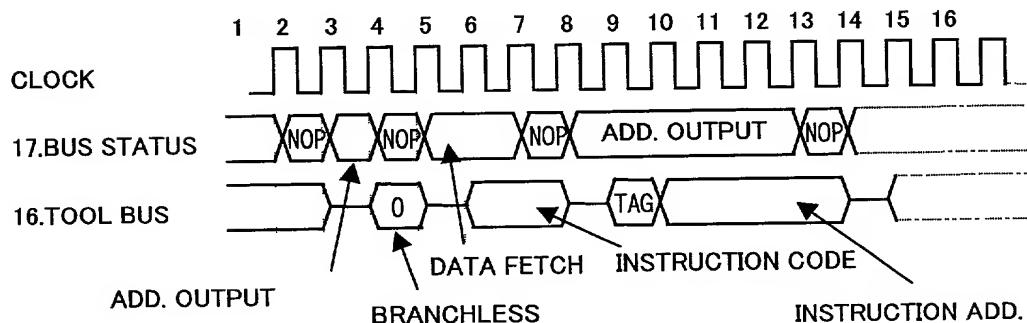


FIG. 4A

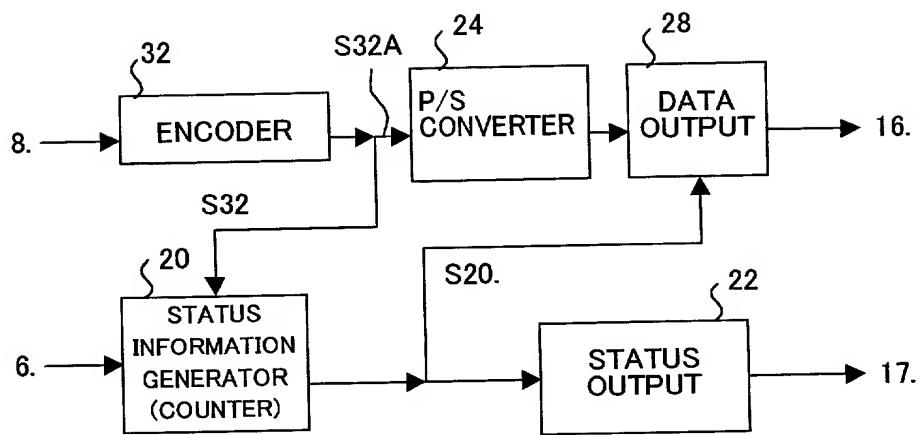


FIG. 4B

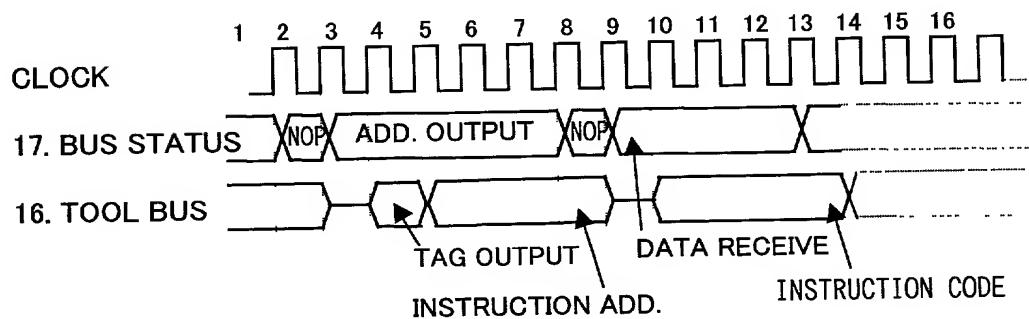


FIG. 5

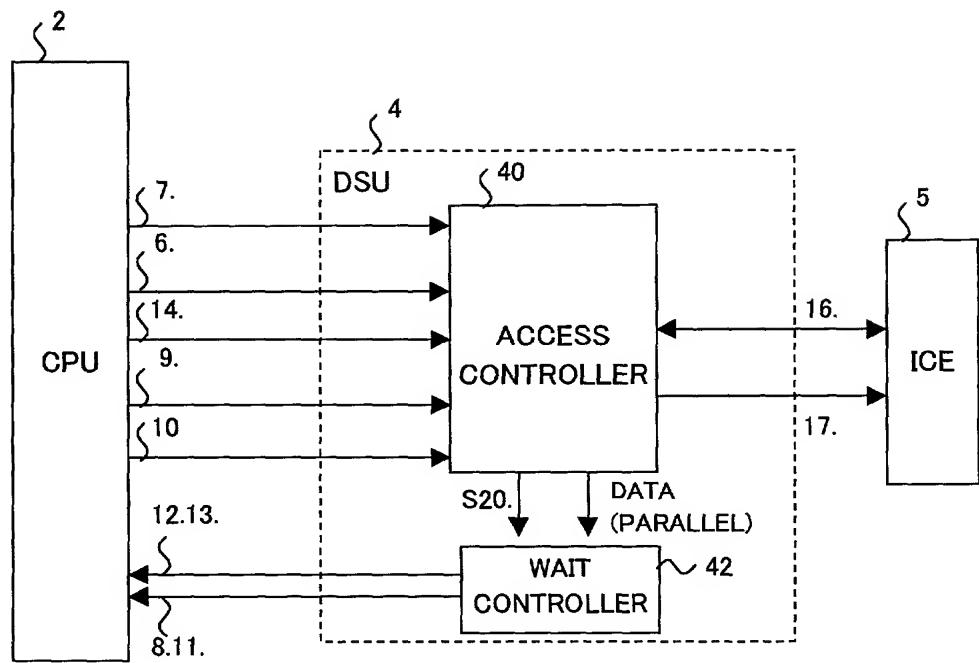


FIG. 6

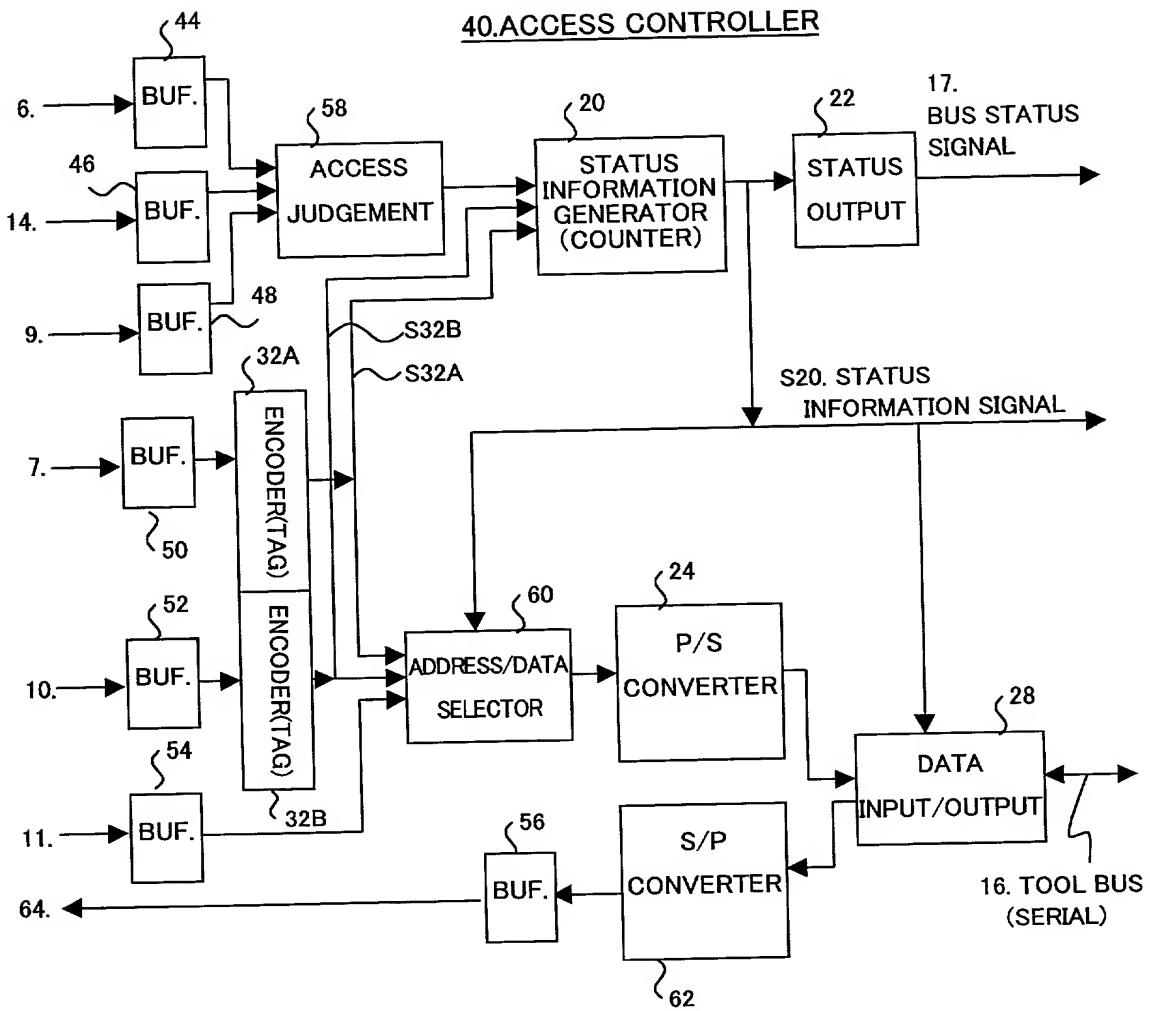


FIG. 7

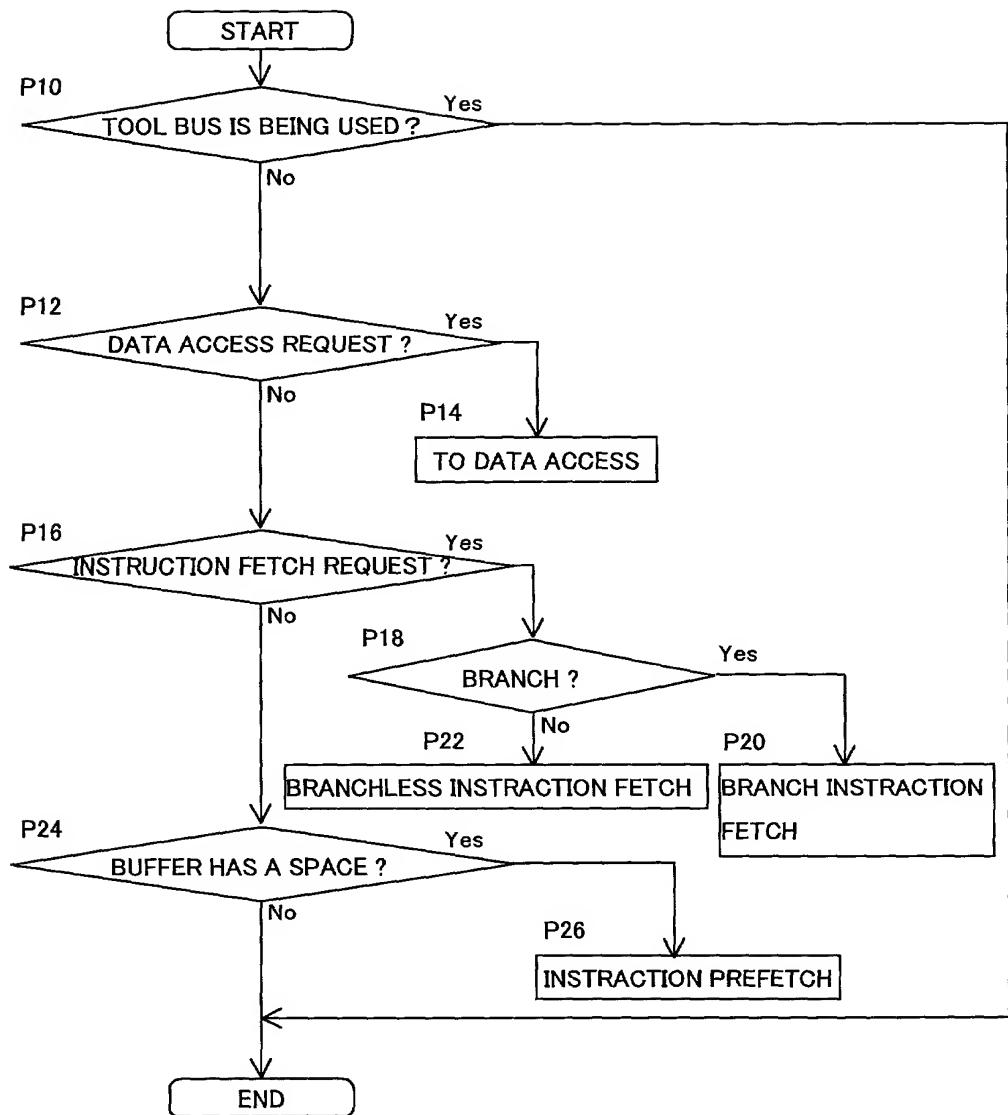


FIG. 8

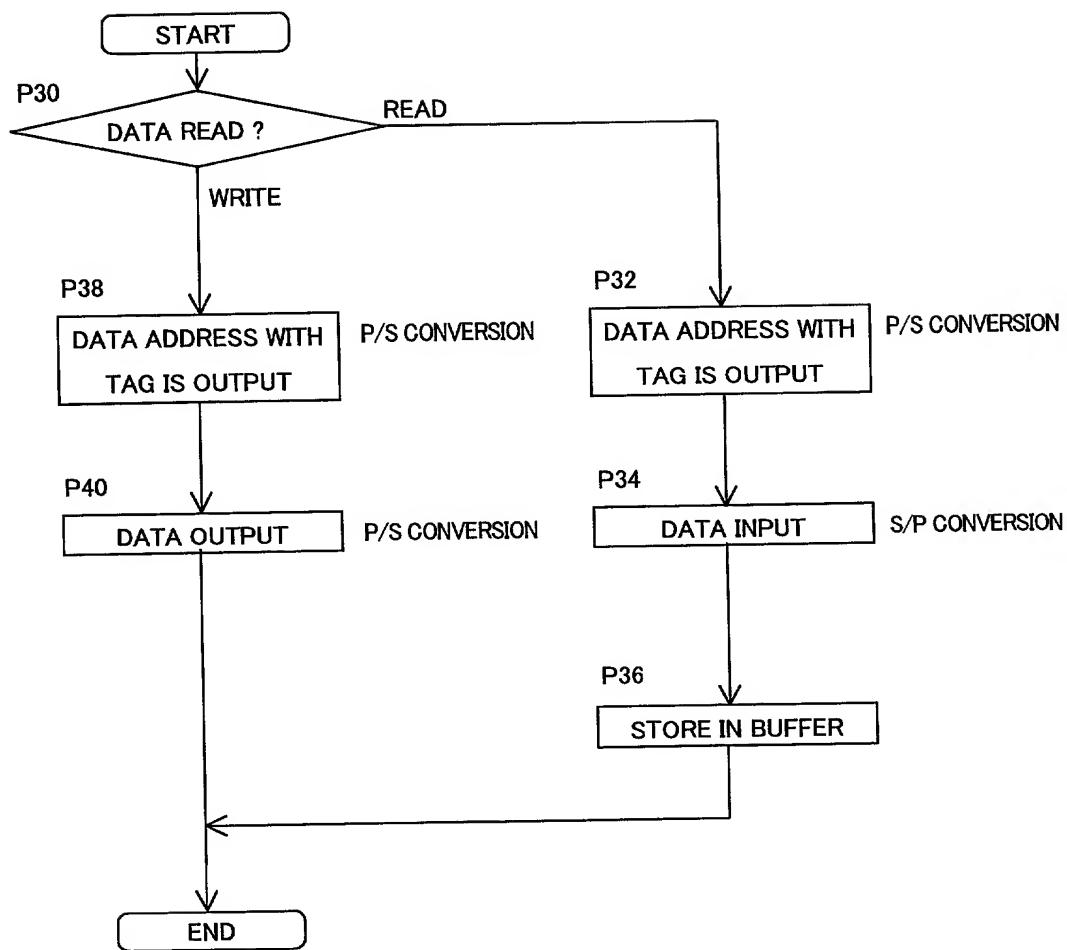


FIG. 9

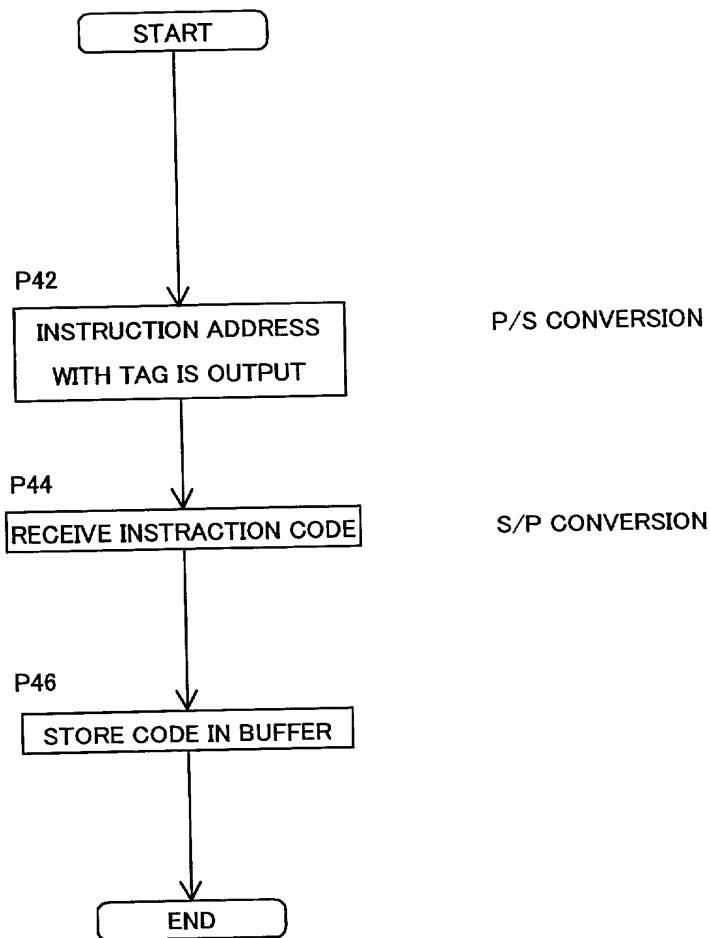


FIG. 10

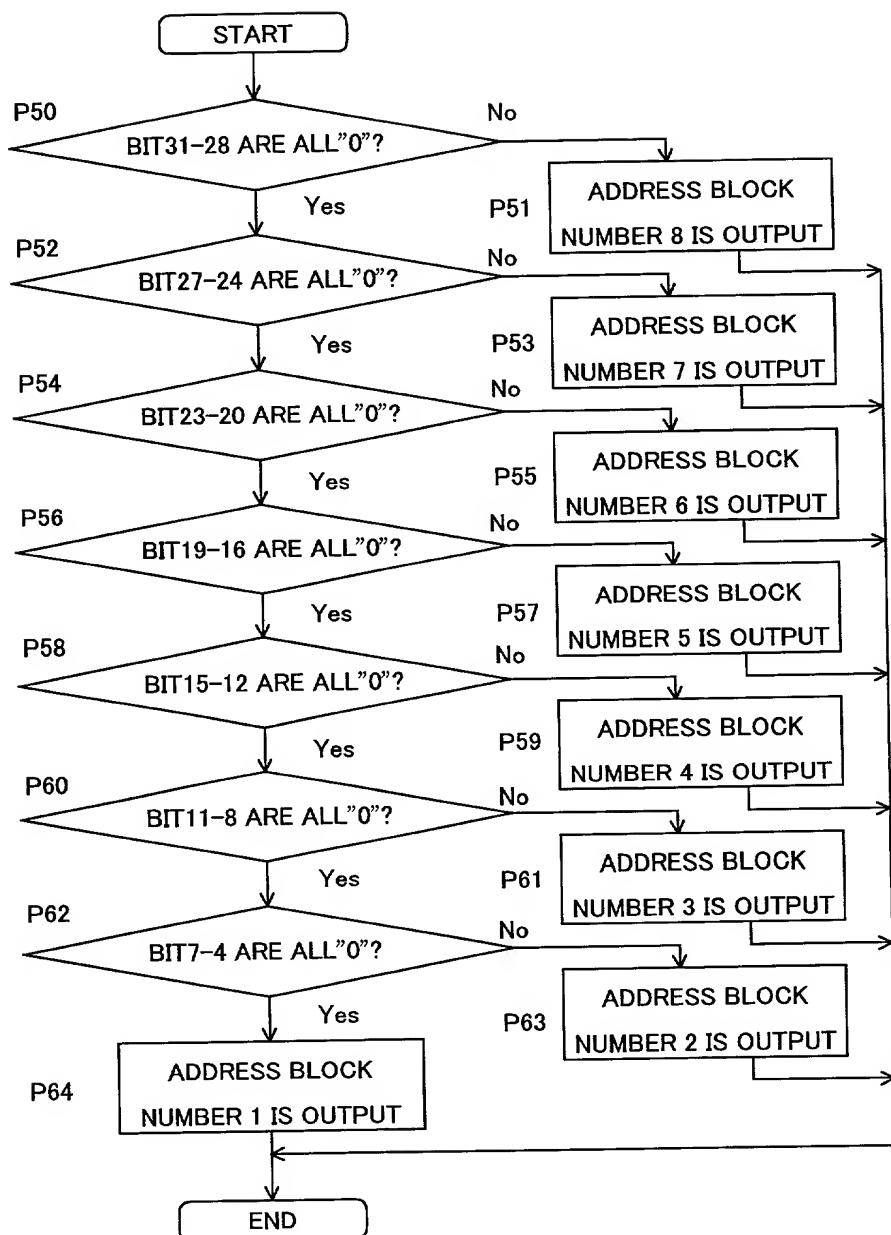


FIG. 11

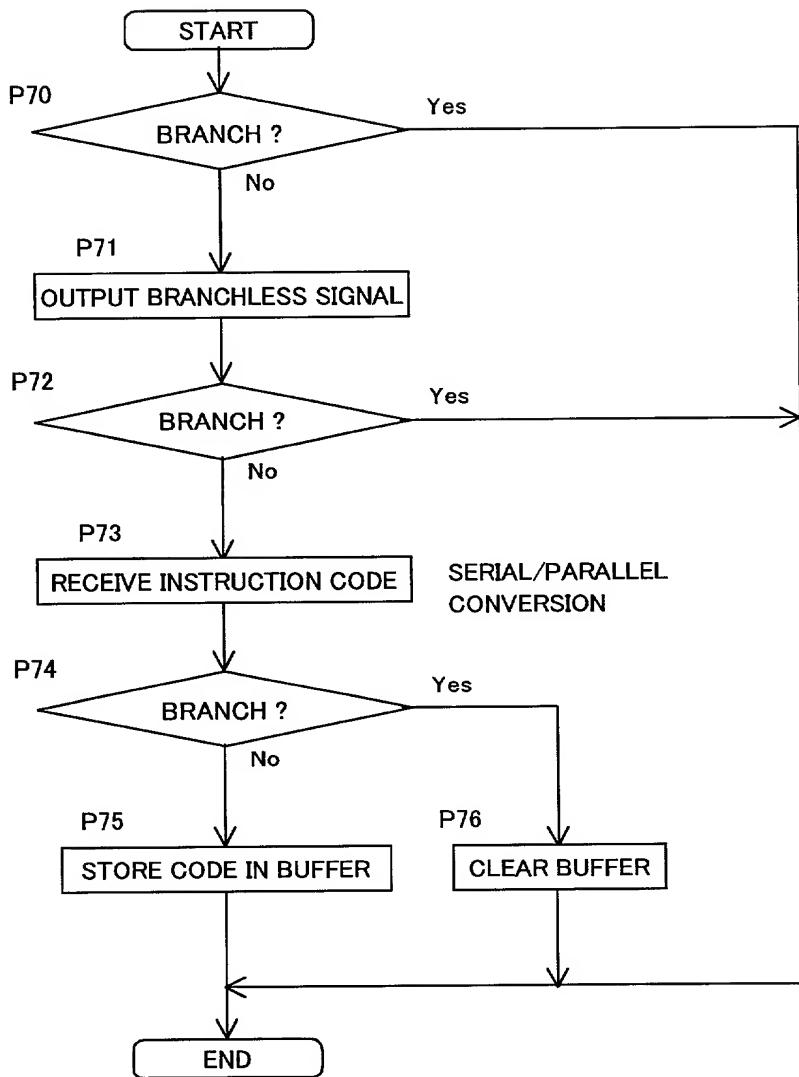


FIG. 12

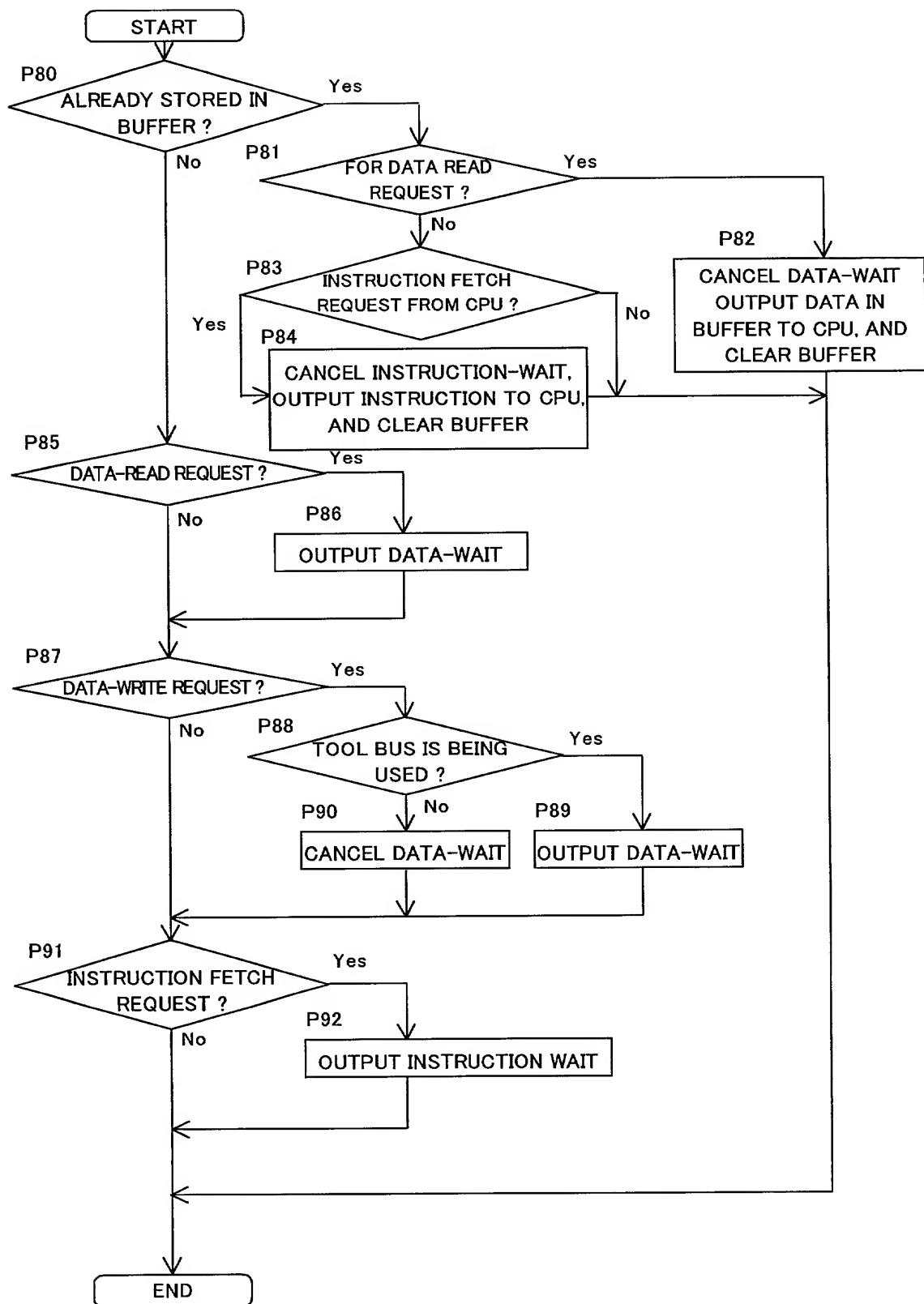


FIG. 13

INSTRUCTION PREFETCH OPERATION  
WITH NO BRANCH BEING GENERATED

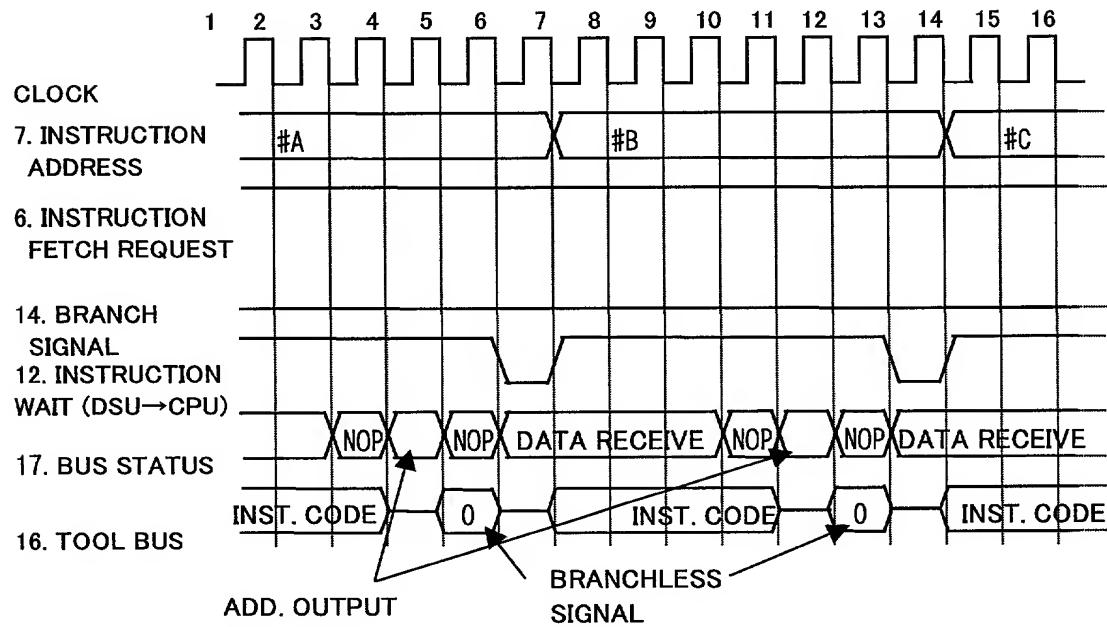
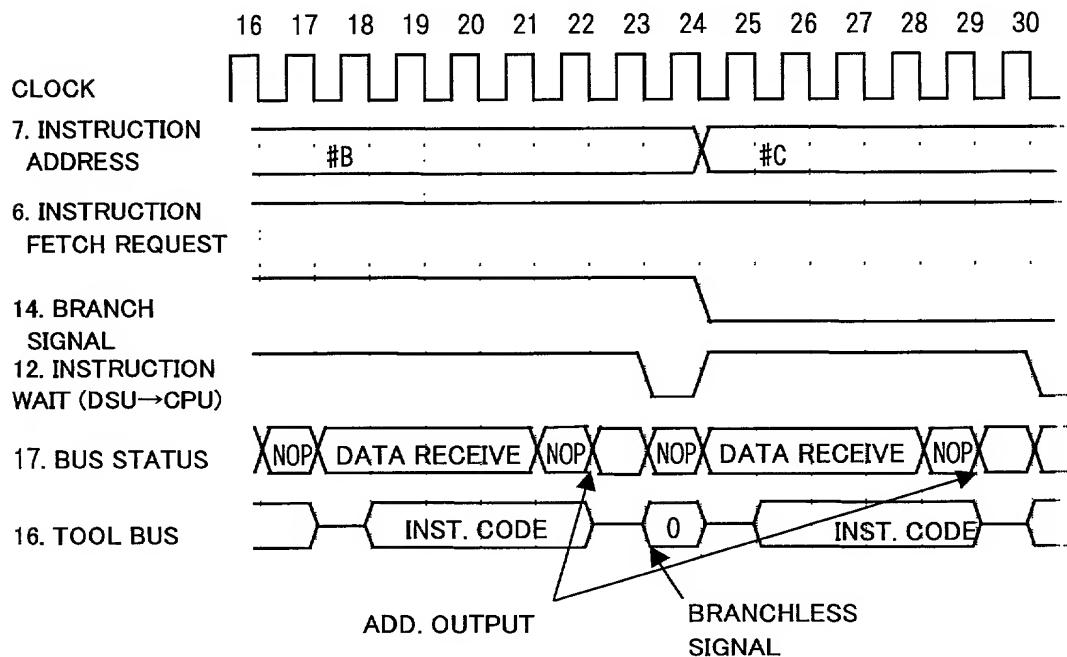
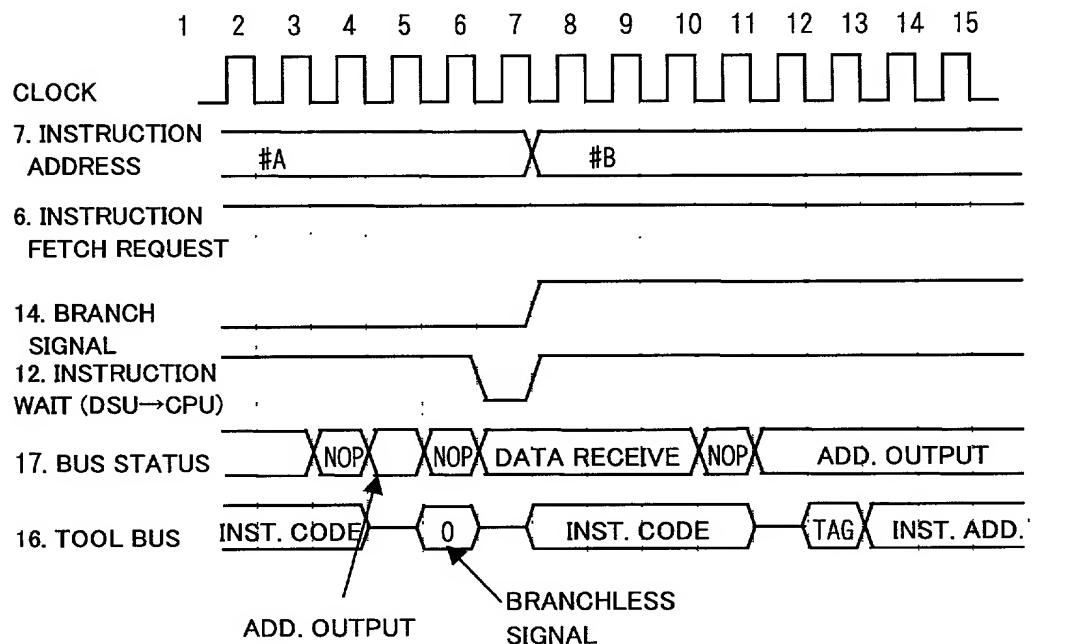


FIG. 14

INSTRUCTION PREFETCH OPERATION WITH BRANCH BEING GENERATED (NO INSTRUCTION PREFETCH IS CANCELLED)



**FIG. 15**

**INSTRUCTION PREFETCH OPERATION WITH BRANCH BEING GENERATED (INSTRUCTION PREFETCH IS CANCELLED)**

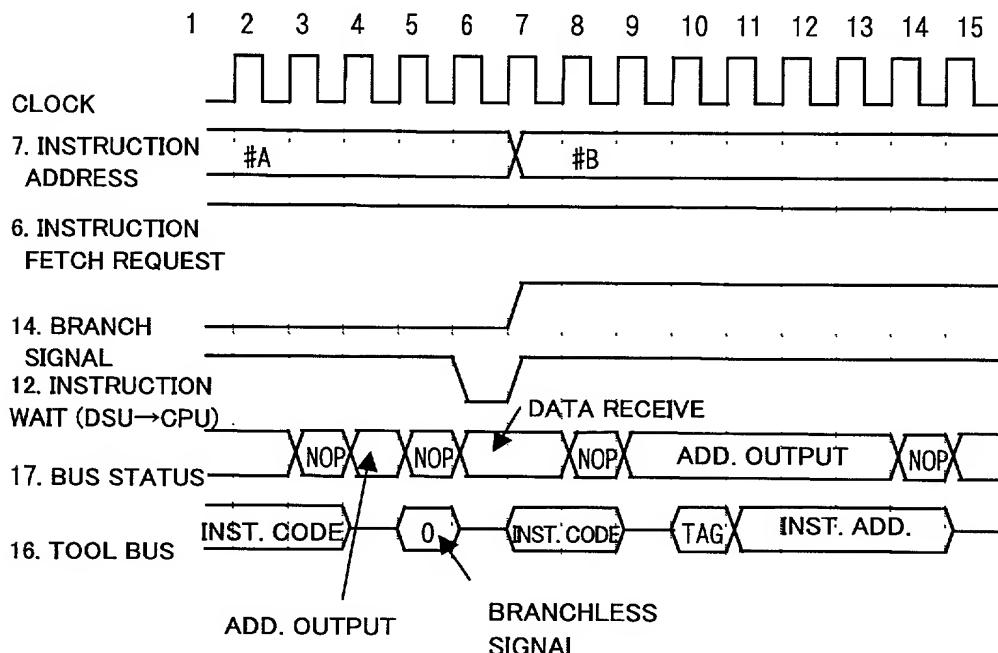


FIG. 16

INSTRUCTION FETCH AND DATA-ACCESS OPERATION  
( DATA-READ)

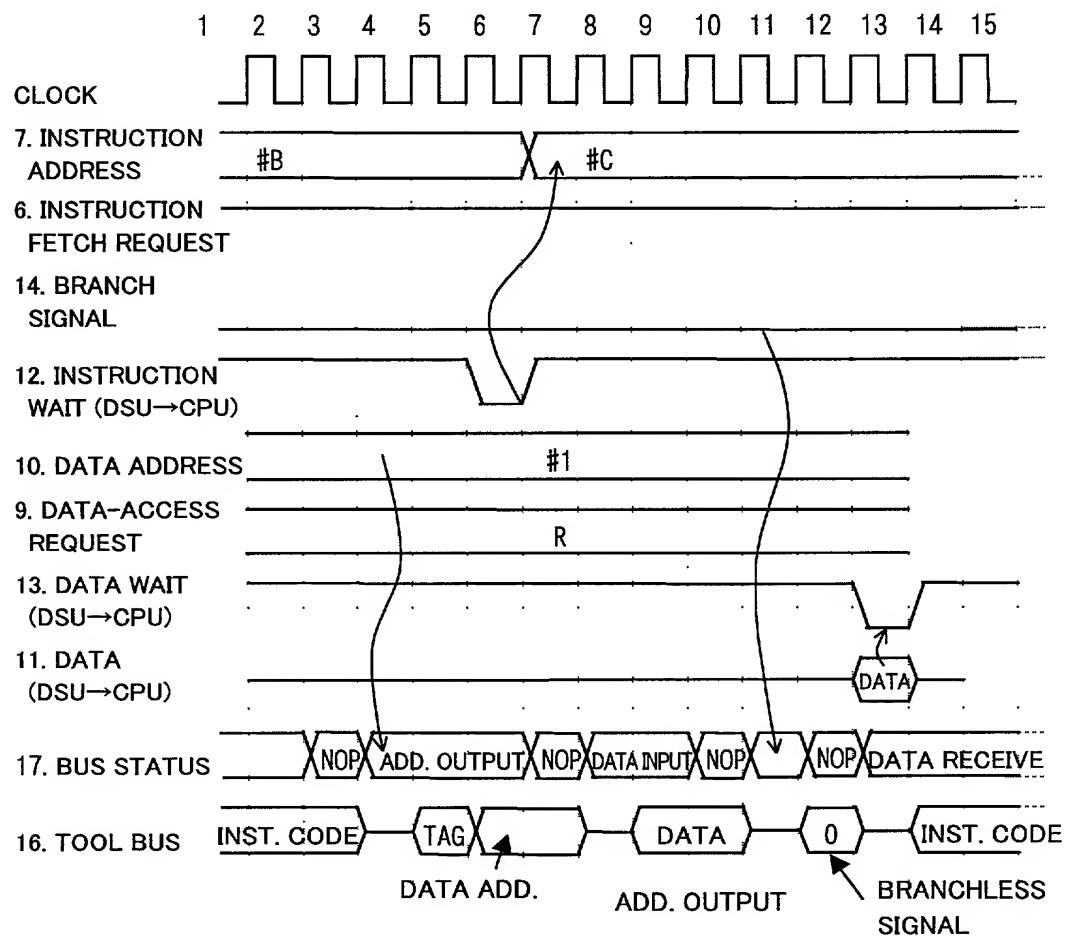


FIG. 17

INSTRUCTION FETCH AND DATA-ACCESS OPERATION  
( DATA-WRITE)

